

MIPS

Reference Guide

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Data Registers

MIPS contains 32 registers for programmers to use:

#	Register(s)	Usage
0	\$zero	→ Hard-wired to 0
1	\$at	→ Reserved for assembler
2,3	\$v0, \$v1	→ Used to store returned values from function calls
4-7	\$a0 - \$a3	→ Used to store values passed as arguments to functions
8-15	\$t0 - \$t7	→ Temporary registers
16-23	\$s0 - \$s7	→ Saved temporary registers
24,25	\$t8, \$t9	→ Temporary registers
26, 27	\$k0, \$k1	→ Reserved for operating system kernel
28	\$gp	→ Global pointer
29	\$sp	→ Stack pointer
30	\$fp	→ Frame pointer
31	\$ra	→ Return address for function calls

Instruction Register Formats

The MIPS IR register supports three different register formats. They are R (register), I (immediate) and J (jump). All MIPS registers are 32-bit, so each register format is 32 bits wide. They differ in the number and types of fields they contain.

R Format

Op-Code	Rs	Rt	Rd	Rh	Function Code
000000	sssss	ttttt	ddddd	hhhhh	fffff

I Format

Op-Code	Rs	Rt	Immediate
ffffff	sssss	ttttt	iiiiiiiiiiiiiiii

J Format

Op-Code	Target
ffffff	iiiiiiiiiiiiiiiiiiiiiiiiiiiiiiii

The R (register) format consists of five different fields. The 6-bit op-code will always be 000000. Rs, Rt and Rd are 5-bit fields that specify the locations of registers being used. Rs and Rt are sources for the operation. Rd is the destination to store the result. If Rh (shift amount) is not used, it becomes 00000. The last 5 bits are the function code. This tells the computer which type of instruction should be executed.

The I (immediate) format consists of four different fields. The 6-bit op-code determines what type of instruction should be executed. This is similar to the function code in the R-format. The Rs field is the source for the operation. The Rt is the register destination to store the result. The last 16 bits hold the value being applied in the operation.

The J (jump) format consists of only two fields. The 6-bit op-code will always be 00001f. The last 26 bits specify the location being jumped to. These type of instructions are similar to high-level language “go to” commands.

MIPS Instruction Set

ADD	Add	MFLO	Move from \$LO
ADDI	Add immediate	MTHI	Move to \$HI
ADDIU	Add immediate unsigned	MTLO	Move to \$LO
ADDU	Add unsigned	MULT	Multiply
AND	And	MULTU	Multiply unsigned
ANDI	And immediate	NOOP	No operation
BEQ	Branch on equal	NOR	Nor
BGEZ	Branch on ≥ 0	OR	Or
BGEZAL	Branch on ≥ 0 and link	ORI	Or immediate
BGTZ	Branch on > 0	SB	Store byte
BLEZ	Branch on ≤ 0	SH	Store halfword
BLTZ	Branch on < 0	SLL	Shift left logical
BLTZAL	Branch on < 0 and link	SLLV	Shift left logical variable
BNE	Branch on $\neq 0$	SLT	Set on less than
DIV	Divide	SLTI	Set on less than immediate
DIVU	Divide unsigned	SLTIU	Set on less than immediate unsigned
J	Jump	SLTU	Set on less than unsigned
JAL	Jump and link	SRA	Shift right arithmetic
JALR	Jump and link register	SRAV	Shift right arithmetic variable
JR	Jump register	SRL	Shift right logical
LB	Load byte	SRLV	Shift right logical variable
LBU	Load byte unsigned	SUB	Subtract
LH	Load halfword	SUBU	Subtract unsigned
LHU	Load halfword unsigned	SW	Store word
LUI	Load upper immediate	SWL	Store word left
LW	Load word	SWR	Store word right
LWL	Load word left	SYSCALL	System call
LWR	Load word right	XOR	Xor
MFHI	Move from \$HI	XORI	Xor immediate

MIPS Instruction Set (Extended)

ADD	add \$d, \$s, \$t	<i>add</i>
<i>additional info</i>	Meaning → \$d = \$s + \$t Function Code → 100000	
ADDI	addi \$t, \$s, imm	<i>add immediate</i>
<i>additional info</i>	Meaning → \$t = \$s + imm Op-Code → 001000	
ADDIU	addiu \$t, \$s, imm	<i>add immediate unsigned</i>
<i>additional info</i>	Meaning → \$t = \$s + imm(unsigned) Op-Code → 001001	
ADDU	addu \$d, \$s, \$t	<i>add unsigned</i>
<i>additional info</i>	Meaning → \$d = \$s + \$t Function Code → 100001	
AND	and \$d, \$s, \$t	<i>and</i>
<i>additional info</i>	Meaning → \$d = \$s and \$t Function Code → 100100	
ANDI	andi \$t, \$s, imm	<i>and immediate</i>
<i>additional info</i>	Meaning → \$t = \$s and imm Op-Code → 001100	
BEQ	beq \$s, \$t, offset	<i>branch on equal</i>
<i>additional info</i>	Meaning → if \$s == \$t branch to offset Op-Code → 000100	
BGEZ	bgez \$s, offset	<i>branch >= zero</i>
<i>additional info</i>	Meaning → if \$s >= 0 branch to offset Op-Code → 000001 Rt → 00001	
BGEZAL	bgezal \$s, offset	<i>branch >= zero and link</i>
<i>additional info</i>	Meaning → if \$s >= 0 branch to offset save return address in \$ra Op-Code → 000001 Rt → 10001	
BGTZ	bgtz \$s, offset	<i>branch > zero</i>
<i>additional info</i>	Meaning → if \$s > 0 branch to offset Op-Code → 000111 Rt → 00000	
BLEZ	blez \$s, offset	<i>branch <= zero</i>
<i>additional info</i>	Meaning → if \$s <= 0 branch to offset Op-Code → 000110 Rt → 00000	

	BLTZ	bltz \$s, offset	<i>branch < zero</i>
<i>additional info</i>		Meaning →	if \$s < 0 branch to offset
		Op-Code →	000001
		Rt →	00000
	BLTZAL	bltzal \$s, offset	<i>branch < zero and link</i>
<i>additional info</i>		Meaning →	if \$s < 0 branch to offset save return address in \$ra
		Op-Code →	000001
		Rt →	10000
	BNE	bne \$s, \$t, offset	<i>branch on not equal</i>
<i>additional info</i>		Meaning →	if \$s != \$t branch to offset
		Op-Code →	000101
	DIV	div \$s, \$t	<i>divide</i>
<i>additional info</i>		Meaning →	\$LO = \$s / \$t \$HI = \$s % \$t
		Function Code →	011010
	DIVU	divu \$s, \$t	<i>divide unsigned</i>
<i>additional info</i>		Meaning →	\$LO = \$s / \$t \$HI = \$s % \$t
		Function Code →	011011
	J	j target	<i>jump</i>
<i>additional info</i>		Meaning →	Jump to target location
		Op-Code →	000010
	JAL	jal target	<i>jump and link</i>
<i>additional info</i>		Meaning →	Jump to target location save return address in \$ra
		Op-Code →	000011
	JALR	jal \$d, \$s	<i>jump and link register</i>
<i>additional info</i>		Meaning →	Jump to location specified by \$s Save return address in \$d
		Function Code →	001001
	JR	jr \$s	<i>jump register</i>
<i>additional info</i>		Meaning →	Jump to target location contained in register \$s
		Function Code →	001000
	LB	lb \$t, offset(\$s)	<i>load byte</i>
<i>additional info</i>		Meaning →	\$t = [\$s + offset]
		Op-Code →	100000
	LBU	lbu \$t, offset(\$s)	<i>load byte unsigned</i>
<i>additional info</i>		Meaning →	\$t = [\$s + offset]
		Op-Code →	100100

LH	lh \$t, offset(\$s)	<i>load halfword</i>
<i>additional info</i>	Meaning → \$t = halfword [\$s + offset] Op-Code → 100001	
LHU	lhu \$t, offset(\$s)	<i>load halfword unsigned</i>
<i>additional info</i>	Meaning → \$t = halfword [\$s + offset] Op-Code → 100101	
LUI	lb \$t, imm	<i>load upper immediate</i>
<i>additional info</i>	Meaning → \$t = imm after imm is shifted left 16 bits Op-Code → 001111	
LW	lw \$t, offset(\$s)	<i>load word</i>
<i>additional info</i>	Meaning → \$t = [\$s + offset] Op-Code → 100011	
LWL	lwl \$t, offset(\$s)	<i>load word left</i>
<i>additional info</i>	Meaning → \$t = [\$s + offset] Op-Code → 100010	
LWR	lwr \$t, offset(\$s)	<i>load word right</i>
<i>additional info</i>	Meaning → \$t = [\$s + offset] Op-Code → 100110	
MFHI	mfhi \$d	<i>move from HI</i>
<i>additional info</i>	Meaning → \$d = \$HI Function Code → 010000	
MFLO	mflo \$d	<i>move from LO</i>
<i>additional info</i>	Meaning → \$d = \$LO Function Code → 010010	
MTHI	mfhi \$s	<i>move to HI</i>
<i>additional info</i>	Meaning → \$HI = \$s Function Code → 010001	
MTLO	mtlo \$s	<i>move to LO</i>
<i>additional info</i>	Meaning → \$LO = \$s Function Code → 010011	
MULT	mult \$s, \$t	<i>multiply</i>
<i>additional info</i>	Meaning → \$LO = \$s * \$t Function Code → 011000	
MULTU	multu \$s, \$t	<i>multiply unsigned</i>
<i>additional info</i>	Meaning → \$LO = \$s * \$t Function Code → 011001	
NOOP	noop	<i>no operation</i>
	Meaning → no operation	

NOR	nor \$d, \$s, \$t	<i>nor</i>
<i>additional info</i>	Meaning → \$d = \$s nor \$t Function Code → 100111	
OR	or \$d, \$s, \$t	<i>or</i>
<i>additional info</i>	Meaning → \$d = \$s or \$t Function Code → 100101	
ORI	ori \$t, \$s, imm	<i>or immediate</i>
<i>additional info</i>	Meaning → \$t = \$s or imm Op-Code → 001101	
SB	sb \$t, offset(\$s)	<i>store byte</i>
<i>additional info</i>	Meaning → [\$s + offset] = least significant bit of \$t Op-Code → 101000	
SH	sh \$t, offset(\$s)	<i>store halfword</i>
<i>additional info</i>	Meaning → [\$s + offset] = half word \$t Op-Code → 101001	
SLL	sll \$d, \$t, h	<i>shift left logical</i>
<i>additional info</i>	Meaning → \$d = \$t shifted left h times Function Code → 000000	
SLLV	sllv \$d, \$t, \$s	<i>shift left logical variable</i>
<i>additional info</i>	Meaning → \$d = \$t shifted left # times in \$s Function Code → 000100	
SLT	slt \$d, \$s, \$t	<i>set on less than</i>
<i>additional info</i>	Meaning → if \$s < \$t then \$d = 1 else \$d = 0 Function Code → 101010	
SLTI	slti \$t, \$s, imm	<i>set on less than immediate</i>
<i>additional info</i>	Meaning → if \$s < imm then \$t = 1 else \$t = 0 Op-Code → 001010	
SLTIU	sltiu \$t, \$s, imm	<i>SLT immediate unsigned</i>
<i>additional info</i>	Meaning → if \$s < imm then \$t = 1 else \$t = 0 Op-Code → 001011	
SLTU	sltu \$d, \$s, \$t	<i>set on less than unsigned</i>
<i>additional info</i>	Meaning → if \$s < \$t then \$d = 1 else \$d = 0 Function Code → 101011	

SRA	sra \$d, \$t, h	<i>shift right arithmetic</i>
	Meaning →	\$d = \$t shifted right h times
<i>additional info</i>	Function Code →	000011
SRAV	srav \$d, \$t, \$s	<i>shift right arith. variable</i>
	Meaning →	\$d = \$t shifted right # times in \$s
<i>additional info</i>	Function Code →	000111
SRL	srl \$d, \$t, h	<i>shift right logical</i>
	Meaning →	\$d = \$t shifted right h times
<i>additional info</i>	Function Code →	000010
SRLV	srlv \$d, \$t, \$s	<i>shift right logical variable</i>
	Meaning →	\$d = \$t shifted right # times in \$s
<i>additional info</i>	Function Code →	000110
SUB	sub \$d, \$s, \$t	<i>subtract</i>
	Meaning →	\$d = \$s - \$t
<i>additional info</i>	Function Code →	100010
SUBU	subu \$d, \$s, \$t	<i>subtract unsigned</i>
	Meaning →	\$d = \$s - \$t
<i>additional info</i>	Function Code →	100011
SW	sw \$t, offset(\$s)	<i>store word</i>
	Meaning →	[\$s + offset] = \$t
<i>additional info</i>	Op-Code →	101011
SWL	swl \$t, offset(\$s)	<i>store word left</i>
	Meaning →	[\$s + offset] = \$t
<i>additional info</i>	Op-Code →	101010
SWR	swr \$t, offset(\$s)	<i>store word right</i>
	Meaning →	[\$s + offset] = \$t
<i>additional info</i>	Op-Code →	101110
SYSCALL	syscall	<i>system call</i>
	Meaning →	Sends an interrupt
<i>additional info</i>	Function Code →	001100
XOR	xor \$d, \$s, \$t	<i>exclusive or</i>
	Meaning →	\$d = \$s xor \$t
<i>additional info</i>	Function Code →	100110
XORI	xori \$t, \$s, imm	<i>exclusive or immediate</i>
	Meaning →	\$t = \$s xor imm
<i>additional info</i>	Op-Code →	001110

SPIM Programming

Every program written in SPIM needs a data and text segment.

```
#.data signifies the beginning of the data segment
.data

#.text starts the "text" portion of the program
.text
```

Within the data segment you can initialize your variables. All variables are initialized in the form:

Name: .Type Content

The name is user defined. It can be any name the programmer wishes to call the variable by. The variable types are the following:

.ascii	→	ASCII string
.asciiz	→	ASCII string followed by a null terminator
.byte	→	Byte
.double	→	Double
.float	→	Float
.word	→	Word

SPIM can be downloaded for free at
<http://www.cs.wisc.edu/~larus/spim.html>

Program Examples

```
#This program prints to screen the string "Hello World!"

#.data signifies the beginning of the data segment
.data

#If hello is called within the main program it will lead to the string.
#.ascii means that the string is in ASCII format followed by
#a NULL terminator
hello: .ascii "Hello World!"

.globl main

#.text starts the "text" portion of the program
.text

#Start main program
main:

    #Setting register $v0 equal to 4 tells the processor that
    #a string in register $a0 is going to be printed to screen
    li $v0, 4
    #Setting content of $a0 to string hello
    la $a0, hello
    #Calling system to perform output
    syscall
```

```
#This program inputs a number and then displays the number

#Data portion of program
.data

.globl main

#Text portion of program
.text

#Start main program
main:

    #Setting register $v0 to 5 tells the processor that
    #an integer is going to be entered from the keyboard
    li $v0, 5
    #calling system to perform input
    syscall

    #The integer that was entered will now be in
    #register $v0.
    #Moving this value into register $t0.
    move $t0, $v0

    #Setting register $v0 to 1 tells the processor that the
    #contents of register $a0 are going to be printed to the monitor
    li $v0, 1
    #Moving content of register $t0 into register $a0
    move $a0, $t0
    #calling system to perform output
    syscall
```

```

#This program asks the user for two integers and then displays the sum

#Data portion of the program
.data

#Creating ASCII strings for input prompt and output
Msg: .asciiz "Enter in an integer: "
Msg2: .asciiz "The sum is: "
#Creating ASCII string for a carriage return
return: .asciiz "\n"

.globl main

#Text portion of the program
.text
#Starting main program
main:

    #Print to screen string "Enter in an integer: "
    li $v0, 4
    la $a0, Msg
    syscall

    #Input an integer from keyboard into register $v0
    li $v0, 5
    syscall
    #Move content of register $v0 into register $t0
    move $t0, $v0

    #Print to screen string "Enter in an integer: "
    li $v0, 4
    la $a0, Msg
    syscall

    #Input an integer from keyboard into register $v0
    li $v0, 5
    syscall
    #move content of register $v0 into register $t1
    move $t1, $v0

    #Print to screen string "\n" car carriage return.
    li $v0, 4
    la $a0, return
    syscall

    #Print to screen string "The sum is: "
    li $v0, 4
    la $a0, Msg2
    syscall

    #Adding registers $t0 and $t1 and store sum in $t2
    add $t2,$t0,$t1
    #Move content of register $t2 (the sum) into register $a0
    move $a0, $t2
    #Print to screen content of $a0
    li $v0, 1
    syscall

```

```

#This program asks the user for two numbers and displays their product

#Data portion of the program
.data

#Creating ASCII string for input prompt
msg1: .asciiz "Please enter a number: "
#Creating ASCII string for output
msg2: .asciiz "The product is: "

.globl main

#Text portion of the program
.text

#Starting main program
main:

    #Printing to screen string "Please enter a number: "
    li $v0, 4
    la $a0, msg1
    syscall

    #Input an integer from keyboard into register $v0
    li $v0, 5
    syscall
    #Move content of register $v0 into register $t0
    move $t0, $v0

    #Printing to screen string "Please enter a number: "
    li $v0, 4
    la $a0, msg1
    syscall

    #Input an integer from keyboard into register $v0
    li $v0, 5
    syscall
    #Move content of register $v0 into register $t1
    move $t1, $v0

    #Multiplying $t0 by $t1. Product will be stored in register $L0
    mult $t0, $t1

    #Moving content of $L0 (the product) into register $t2
    mflo $t2

    #Printing to screen string "The product is: "
    li $v0, 4
    la $a0, msg2
    syscall

    #Moving content of $t2 (the product) into register $a0
    move $a0, $t2

    #Printing to screen content of $a0
    li $v0, 1
    syscall

```

```

#This program asks the user for an integer and then determines if it
#is even or odd

#Data portion of the program
.data

#Creating ASCII string for input prompt
question: .asciiz "Please enter an integer: "
#Creating ASCII string for output if the number is even
even: .asciiz "That number is even"
#Creating ASCII string for output if the number is odd
odd: .asciiz "That number is odd"

.globl main

#Text portion of the program
.text

#Starting main program
main:

    #Print to screen the string "Please enter an integer: "
    li $v0, 4
    la $a0, question
    syscall

    #Input an integer from keyboard and store it in register $v0
    li $v0, 5
    syscall
    #Move content of $v0 into register $t0
    move $t0, $v0

    #Load register $t1 with immediate value of 2
    li $t1, 2
    #Divide $t0 by $t1.
    # $t0 % $t1 will be stored in $HI. $t0 / $t1 will be stored in $LO
    div $t0, $t1

    #Move content of $HI into register $t2
    mfhi $t2
    #If register $t2 is 0 (NUM % 2 = 0) then branch to AAA
    beq $t2, $zero, AAA

    #Print to screen string "That number is odd" if haven't branched
    li $v0, 4
    la $a0, odd
    syscall
    #Jump to BBB (to skip message for even number)
    j BBB
#AAA start
AAA:
    #Print to screen string "That number is even"
    li $v0, 4
    la $a0, even
    syscall
#BBB start
BBB:

```